

JCE41 U.S. PRO  
09/723410  
11/27/00

		Subclass	ISSUE CLASSIFICATION

PATENT NUMBER.

**U.S. UTILITY** Patent Application

<b>O.I.P.E.</b> TR SCANNED <u>832</u> Q.A. <u>1C</u>	<b>PATENT DATE</b>
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APPLICATION NO. 09/7234.18	CONT/PRIOR	CLASS 380	SUBCLASS 287	ART UNIT 2131	EXAMINER H. S. Quinonez
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Applicants:  
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**High performance ipsec hardware accelerator for packet classification**

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)	(Date)			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)	(Date)			

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